

FORM PTO-1449
(Rev. 2-32)U.S. Department of Commerce
Patent and Trademark Office

Atty. Docket No.

Serial No.

00-167-C

09/742,250

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use several sheets if necessary)

Applicant:

Werner et al.

Filing Date:

12/20/00

Group:

2819

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appr priat
L	4,748,637	5/31/88	Bishop et al.			
L	5,254,883	10/19/93	Horowitz et al.			
L	5,608,755	3/4/97	Rakib			

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	Subclass	Translation	
					Yes	No
WO 99/10982	3/4/99	PCT				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).

L	Vogt, et al. " 10-Bit High Speed CMOS DAC MACROCELL". IEEE 1989 Custom Integrated Circuits Conference pp. 6.7.1-6.7.4 no month / no date.

EXAMINER	DATE CONSIDERED
<i>L. H. L. L.</i>	4/28/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

FORM PTO-1449 (Rev. 2-22-99)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. 00-167-C	Serial No. 09/742,250
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant: Werner et al.	
		Filing Date: 12/20/00	Group: 2819

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
<i>L</i>	1.	2,912,684	11/10/59	Steele			
<i>L</i>	2.	3,051,901	6/24/58	Yaeger			
	3.	3,078,378	6/28/60	Burley et al.			
	4.	3,267,459	12/18/62	Chomicki et al.			
	5.	3,484,559	7/28/96	Rigby			
	6.	3,508,076	8/26/67	Winder			
	7.	3,510,585	2/2/67	Stone			
	8.	3,560,856	12/26/67	Kaneko			
	9.	3,569,955	3/9/71	Maniere			
	10.	3,571,725	3/23/71	Kaneko			
	11.	3,587,088	6/22/71	Fmszek			
	12.	3,648,064	3/7/72	Mukai et al.			
	13.	3,697,874	10/10/72	Kaneko			
	14.	3,731,199	5/1/73	Tazaki et al.			
	15.	3,733,550	5/15/73	Tazaki et al.			
	16.	3,753,113	8/14/73	Maruta et al.			
<i>L</i>	17.	3,754,237	8/21/73	De Laage de Meux			
	18.	3,761,818	9/25/73	Tazaki et al.			

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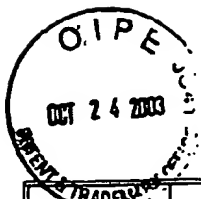


U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
L	19.	3,772,680	11/13/73	Kawai et al.			
H	20.	3,798,544	3/19/74	Norman			
	21.	3,832,490	8/27/74	Leonard			
	22.	3,860,871	1/14/75	Hinoshita et al.			
	23.	3,876,944	4/8/75	Mack et al.			
	24.	3,927,401	12/16/75	McIntosh			
	25.	3,978,284	8/31/76	Yoshino et al.			
	26.	3,988,676	10/26/76	Whang			
	27.	4,038,564	7/26/77	Hakata			
	28.	4,070,650	1/24/78	Ohashi et al.			
	29.	4,086,587	4/25/78	Lender			
	30.	4,097,859	6/27/78	Looschen			
	31.	4,131,761	12/26/78	Giusto			
	32.	4,181,865	1/1/80	Kohyama			
	33.	4,280,221	7/21/81	Chun et al.			
	34.	4,373,152	2/8/83	Jacobsthal			
	35.	4,382,249	5/3/83	Jacobsthal			
	36.	4,403,330	9/8/83	Meyer			
	37.	4,408,135	10/4/83	Yuyama et al.			
	38.	4,408,189	10/4/83	Betts et al.			
	39.	4,438,491	3/20/84	Constant			
	40.	4,481,625	11/6/84	Roberts et al.			
	41.	4,528,550	7/9/85	Graves et al.			
	42.	4,571,735	2/18/86	Furse			
	43.	4,602,374	7/22/86	Nakamura et al.			
	44.	4,620,188	10/28/86	Sengehanh			
L	45.	4,628,297	12/9/86	Mita et al.			

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U.S. PATENT DOCUMENTS

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<i>h</i>	46. 4,748,637	5/31/88	Bishop			
<i>n</i>	47. 4,779,073	10/18/88	Iketani			
	48. 4,805,190	2/14/89	Jaffre et al.			
	49. 4,821,286	4/11/89	Graczyk et al.			
	50. 4,823,028	4/18/89	Lloyd			
	51. 4,825,450	4/25/89	Herzog			
	52. 4,841,301	6/20/89	Ichihara			
	53. 4,860,309	8/22/89	Costello			
	54. 4,875,049	10/17/89	Yoshida			
	55. 4,888,764	12/19/89	Haug			
	56. 5,003,555	3/26/91	Bergmans			
	57. 5,023,488	6/11/91	Gunning			
	58. 5,023,841	6/11/91	Akrouf et al.			
	59. 5,045,728	9/3/91	Crafts			
	60. 5,046,050	9/3/91	Kertis			
	61. 5,115,450	5/19/92	Arcuri			
	62. 5,121,411	6/9/92	Flurharty			
	63. 5,126,974	8/30/92	Sasaki et al.			
	64. 5,153,459	10/6/92	Park et al.			
	65. 5,172,338	12/15/92	Mehrotra et al.			
	66. 5,191,330	3/2/93	Fisher et al.			
	67. 5,194,765	3/16/93	Dunlop et al.			
	68. 5,230,008	7/20/93	Duch et al.			
	69. 5,243,625	9/7/93	Verbakel et al.			
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<i>h</i>	71. 5,259,002	11/2/93	Carlstedt			
	72. 5,280,500	1/18/94	Mazzola et al.			

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S/N: 00742,250
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U.S. PATENT DOCUMENTS

Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
L	73. 5,287,108	2/15/94	Mayes et al			
L	74. 5,295,155	3/15/94	Gersbach et al.			
	75. 5,295,157	3/15/94	Suzuki et al.			
	76. 5,315,175	5/24/94	Langner			
	77. 5,331,320	7/19/94	Cideciyan et al ;			
	78. 5,373,473	12/13/94	Okumura			
	79. 5,408,498	4/18/95	Yoshida			
	80. 5,412,689	5/2/95	Chan et al			
	81. 5,425,056	6/13/95	Maroun et al			
	82. 5,426,739	6/20/95	Lin et al.			
	83. 5,459,749	10/17/95	Park			
	84. 5,471,156	11/28/95	Kim et al.			
	85. 5,473,635	12/5/95	Chevroulet			
	86. 5,483,110	1/9/96	Koide et al.			
	87. 5,438,593	8/1/95	Karam et al.			
	88. 5,508,570	4/16/96	Laber et al.			
	89. 5,513,327	4/30/96	Farmwald et al.			
	90. 5,525,983	8/11/96	Patel et al.			
	91. 5,534,795	7/9/96	Wert			
	92. 5,534,798	7/9/96	Phillips et al.			
	93. 5,539,774	7/23/96	Nobakht et al			
	94. 5,553,097	9/3/96	Dagher			
	95. 5,596,439	1/21/97	Dankberg et al.			
	96. 5,608,755	3/4/97	Rakib			
	97. 5,633,631	5/27/97	Teckman			
L	98. 5,663,663	9/2/97	Cao et al.			
L	99. 5,640,605	6/17/97	Johnson et al.			

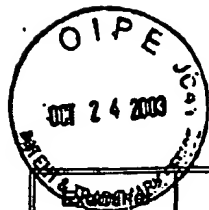
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U.S. PATENT DOCUMENTS

Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
<i>h</i>	100, 5,644,253	7/1/97	Takatsu			
<i>h</i>	101, 5,684,833	11/4/97	Watanabe			
	102, 5,984,424	12/2/97	Ariyavisitakul			
	103, 5,734,294	3/31/98	Bezzam et al.			
	104, 5,740,201	4/14/98	Hui			
	105, 5,742,591	4/21/98	Himayat et al.			
	106, 5,751,168	5/12/98	Speed III et al.			
	107, 5,757,712	5/26/98	Nagel et al.			
	108, 5,761,246	6/2/98	Cao et al.			
	109, 5,793,815	8/11/98	Goodnow et al.			
	110, 5,793,816	8/11/98	Hui			
	111, 5,796,781	8/18/98	DeAndrea et al.			
	112, 5,798,918	8/25/98	Georgiou et al.			
	113, 5,809,033	9/15/98	Turner et al.			
	114, 5,825,825	10/20/98	Altmann et al.			
	115, 5,864,584	1/26/99	Cao et al.			
	116, 5,867,010	2/2/99	Hinedi et al.			
	117, 5,872,468	2/16/99	Dyke			
	118, 5,887,032	3/23/99	Cioffi			
	119, 5,892,466	4/6/99	Walker			
	120, 5,898,734	4/27/99	Nakamura et al.			
	121, 5,917,340	6/29/99	Manohar et al.			
	122, 5,917,856	6/29/99	Torsti			
	123, 5,933,458	8/3/99	Leurent et al.			
	124, 5,942,994	8/24/99	Lewiner et al.			
<i>h</i>	125, 5,946,355	8/31/99	Baker			
	126, 5,949,280	9/7/99	Sasaki			

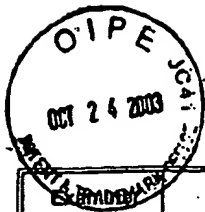
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U.S. PATENT DOCUMENTS

Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
<i>h</i>	127, 5,969,579	10/19/99	Hartke et al.			
<i>h</i>	128, 5,969,648	10/19/99	Garnett			
	129, 5,970,088	10/19/99	Chen			
	130, 5,973,508	10/26/99	Nowak et al.			
	131, 5,977,798	11/2/99	Zerbe			
	132, 5,982,741	11/9/99	Ethier			
	133, 5,986,472	11/16/99	Hinedi et al.			
	134, 6,005,895	12/21/99	Perino et al.			
	135, 6,006,169	12/21/99	Sandhu et al.			
	136, 6,009,120	12/28/99	Nobakht			
	137, 6,018,550	1/25/00	Emma et al			
	138, 6,034,993	3/7/00	Norrell et al.			
	139, 6,038,260	3/14/00	Emma et al			
	140, 6,048,931	4/11/00	Fujita et al.			
	141, 6,049,229	4/11/00	Manohar et al			
	142, 6,052,390	4/18/00	Deliot et al			
	143, 6,061,395	5/9/00	Tonami			
	144, 6,067,326	5/23/00	Jonsson et al.			
	145, 6,078,627	6/20/00	Crayford			
	146, 6,094,461	7/25/00	Heron			
	147, 6,097,215	8/1/00	Bialas Jr et al			
	148, 6,101,561	8/8/00	Beers et al.			
	149, 6,114,979	9/5/00	Kim			
	150, 6,122,010	9/19/00	Emelko			
	151, 6,140,841	10/31/00	Suh			
<i>L</i>	152, 6,160,421	12/12/00	Barna			
	153, 6,195,397	2/27/01	Kwon			

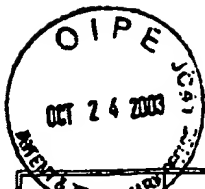
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U.S. PATENT DOCUMENTS

Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
<i>L</i>	154. 6,204,785	3/20/01	Fattaruso			
<i>L</i>	155. 6,215,635	4/10/01	Nguyen			
<i>L</i>	156. 6,222,380	4/24/01	Gerowitz			
<i>L</i>	157. 6,307,824	10/23/01	Kuroda et al			
<i>L</i>	158. RE 30,182	12/25/79	Howson			
<i>L</i>	159. 5,825,450	10/28/98	Date et al.			
<i>L</i>	160. 5,804,468	2/18/97	Gillig			
<i>L</i>	161. 5,546,042	8/13/96	Tedrow et al.			
<i>L</i>	162. 6,094,075	7/25/00	Garrett Jr et al.			
<i>L</i>	163. 6,069,577	5/30/00	Morisson et al.			

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		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
<i>L</i>	164.	WO 95/31867	11/23/95	PCT				
<i>A</i>	165.	WO 96/31038	10/3/96	PCT				
	166.	WO 99/10982	3/4/99	PCT				
	167.	EP 0 352 869	1/31/90	EP				
	168.	EP 0 482 392 with translation	4/29/92	EP				
	169.	EP 0 490 504	7/17/92	EP				
	170.	EP 0 094 624	11/23/83	EP				
	171.	43 20 930	1/5/95	DE				
	172.	358070662	4/27/83	JP				
	173.	362051329	3/6/87	JP				
	174.	354051343	4/23/79	JP				
<i>V</i>	175.	356164650	12/17/81	JP				
<i>L</i>	176.	359036465	2/28/84	JP				

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FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
2	177. 360087551	5/17/85	JP				
	178. 360194647	10/3/85	JP				
	179. 408286943	11/1/96	JP				
	180. 409181778	7/11/97	JP				
	181. 354060850	5/16/79	JP				
	182. 408202677	8/9/96	JP				
	183. 405143211	6/11/93	JP				
	184. 404044691	2/14/92	JP				
	185. 402140676	5/30/90	JP				
	186. 402128201	5/16/90	JP				
	187. 10200345	7/13/98	JP				
	188. WO 98/33306	7/30/98	PCT				
	189. EP 0 463 316	1/2/92	EP				
2	190. 58-54412	3/3/83	JP				

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	194.	Perez-Alvarez, et al. "A Differential Error Reference Adaptive Echo Canceller for Multilevel PAM Line Codes", IEEE, pp. 1707-1710 (1996). no date/month	
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	196.	Cova, et al. "Characterization of Individual Weights in Transversal Filters and Application to CCD's", IEEE Journal of Solid-State Circuits, Vol. SC-17, pp. 1054-1061 (1982). no date/month	
	197.	Dally, et al., "Multi-Gigabit Signaling with CMOS", May 12, 1997.	
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201.	Aryavisitakul, et al., "Tap-Selected Decision-Feedback Equalization", <i>IEEE Transactions on Communications</i> , pp. 1497-1500 (1997). <i>no month/date</i>
202.	Sidiropoulos, et al., "A 700-Mb/s/pin CMOS Signaling Interface Using Current Integrating Receivers", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, pp. 681-690 (1997). <i>no month/date</i>
203.	Donnelly et al., "A 660 MB/s Interface Megacell Portable Circuit in 0.3 μ m-0.7 μ m CMOS ASIC", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 31, pp. 1995-2003 (1996). <i>no date/month</i>
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207.	Schumacher, et al., "CMOS Subnanosecond True-ECL Output Buffer", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 25, pp. 150-154 (1990). <i>no date/month</i>
208.	Yang, et al., "A 4-ns 4Kx1-bit Two Port BiCMOS SRAM", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 23, No. 5, pp. 1030-1040 (1988). <i>no date/month</i>
209.	Bazes, "Two Novel Fully Complementary Self-Biased CMOS Differential Amplifiers", <i>IEEE Journal of Solid State Circuits</i> , Vol. 28, pp. 165-168, (1991). <i>no date/month</i>
210.	Ishibe, et al. "High-Speed CMOS I/O Buffer Circuits", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 27, pp. 671-677 (1992). <i>no date/month</i>
211.	Lee, et al., "A 80ns 5V-Only Dynamic RAM", <i>ISSCC/79 Proceedings</i> , Paper 12.2 (1979). <i>no date/month</i>
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